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1 [Hybrid dynamic data race detection](#)



Robert O'Callahan, Jong-Deok Choi
 October 2003 ACM SIGPLAN Notices, Volume 38 Issue 10
Publisher: ACM

Full text available: [pdf\(158.47 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
[index terms](#)

We present a new method for dynamically detecting potential data races in multithreaded programs. Our method improves on the state of the art in accuracy, in usability, and in overhead. We improve accuracy by combining two previously known race detection ...

Keywords: Java, dynamic race detection, happens-before, lockset hybrid

2 [Hybrid dynamic data race detection](#)



Robert O'Callahan, Jong-Deok Choi

June PPOPP '03: Proceedings of the ninth ACM SIGPLAN symposium on Principles and practice of parallel programming

Publisher: ACM

Full text available: [pdf\(158.47 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),

[index terms](#)

We present a new method for dynamically detecting potential data races in multithreaded programs. Our method improves on the state of the art in accuracy, in usability, and in overhead. We improve accuracy by combining two previously known race detection ...

Keyw ord s: Java, dynamic race detection, happens-before, lockset hybrid

3 [Eliminating stack overflow by abstract interpretation](#)



John Regehr, Alastair Reid, Kirk Webb

November ACM Transactions on Embedded Computing Systems (TECS),
2005 Volume 4 Issue 4

Publisher: ACM

Full text available: [pdf\(510.78 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),

[index terms](#)

An important correctness criterion for software running on embedded microcontrollers is *stack safety*: a guarantee that the call stack does not overflow. Our first contribution is a method for statically guaranteeing stack safety of interrupt-driven ...

Keyw ord s: Microcontroller, abstract interpretation, call stack, context sensitive, dataflow analysis, interrupt-driven, sensor network

4 [Method-level phase behavior in java workloads](#)



Andy Georges, Dries Buytaert, Lieven Eeckhout, Koen De Bosschere

October ACM SIGPLAN Notices, Volume 39 Issue 10
2004

Publisher: ACM

Full text available: [pdf\(695.63 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),

[index terms](#)

Java workloads are becoming more and more prominent on various computing devices. Understanding the behavior of a Java workload which includes the interaction between the application and the virtual machine (VM), is thus of primary importance during ...

5 [Exploiting temporal consistency to reduce false positives in host-based](#)



[collaborative detection of worms](#)

David J. Malan, Michael D. Smith

November 2006 WORM '06: Proceedings of the 4th ACM workshop on Recurring malware

Publisher: ACM

Full text available: [pdf\(649.50 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The speed of today's worms demands automated detection, but the risk of false positives poses a difficult problem. In prior work, we proposed a host-based intrusion-detection system for worms that leveraged collaboration among peers to lower its risk ...

Keywords: HIDS, IDS, collaborative detection, host-based intrusion detection, native API, peers, system calls, system services, temporal consistency, win32, windows, worms

6 [Bottleneck detection in UMTS via TCP passive monitoring: a real case](#)



Fabio Ricciato, Francesco Vacirca, Martin Karner

October 2005 CoNEXT '05: Proceedings of the 2005 ACM conference on Emerging network experiment and technology

Publisher: ACM

Full text available: [pdf\(469.35 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

In this work we address the problem of inferring the presence of a bottleneck from passive measurement in the UMTS core network. The study is based on one month of packet traces collected in the core network of mobilkom austria AG & Co KG, the leading ...

Keywords: UMTS, bottleneck detection

7 [Method-level phase behavior in java workloads](#)



Andy Georges, Dries Buytaert, Lieven Eeckhout, Koen De Bosschere

October 2004 OOPSLA '04: Proceedings of the 19th annual ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications

Publisher: ACM

Full text available: [pdf\(695.63 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Java workloads are becoming more and more prominent on various computing devices. Understanding the behavior of a Java workload which includes the interaction between the application and the virtual machine (VM), is thus of primary importance during ...

8 [Frame shared memory: line-rate networking on commodity hardware](#)



John Giacomoni, John K. Bennett, Antonio Carzaniga, Douglas C. Sicker, Manish Vachharajani, Alexander L. Wolf

December ANCS '07: Proceedings of the 3rd ACM/IEEE Symposium on Architecture
2007 for networking and communications systems

Publisher: ACM

Full text available: [pdf\(307.52 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Network processors provide an economical programmable platform to handle the high throughput and frame rates of modern and next-generation communication systems. However, these platforms have exchanged general-purpose capabilities for performance. This ...

Keywords: multi-core, multiprocessors, parallel programming, software network processor

9 [Upgrading transport protocols using untrusted mobile code](#)



Parveen Patel, Andrew Whitaker, David Wetherall, Jay Lepreau, Tim Stack
December ACM SIGOPS Operating Systems Review, Volume 37 Issue 5
2003

Publisher: ACM

Full text available: [pdf\(248.86 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
[index terms](#)

In this paper, we present STP, a system in which communicating end hosts use untrusted mobile code to remotely upgrade each other with the transport protocols that they use to communicate. New transport protocols are written in a type-safe version of ...

Keywords: TCP-friendliness, deployment, implementation, transport protocols, untrusted mobile code

10 [Upgrading transport protocols using untrusted mobile code](#)



Parveen Patel, Andrew Whitaker, David Wetherall, Jay Lepreau, Tim Stack
October SOSOP '03: Proceedings of the nineteenth ACM symposium on Operating
2003 systems principles

Publisher: ACM

Full text available: [pdf\(248.86 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
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Keywords: TCP-friendliness, deployment, implementation, transport protocols, untrusted mobile code

11 Symbolic bounds analysis of pointers, array indices, and accessed memory



regions

Radu Rugina, Martin C. Rinard

March 2005 ACM Transactions on Programming Languages and Systems
(TOPLAS), Volume 27 Issue 2

Publisher: ACM

Full text available: [pdf\(490.56 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article presents a novel framework for the symbolic bounds analysis of pointers, array indices, and accessed memory regions. Our framework formulates each analysis problem as a system of inequality constraints between symbolic bound polynomials. ...

Keywords: Symbolic analysis, parallelization, static race detection

12 SECA: security-enhanced communication architecture



Joel Coburn, Srivaths Ravi, Anand Raghunathan, Srimat Chakradhar

September 2005 CASES '05: Proceedings of the 2005 international conference on
Compilers, architectures and synthesis for embedded systems

Publisher: ACM

Full text available: [pdf\(396.53 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this work, we propose and investigate the idea of enhancing a System-on-Chip (SoC) communication architecture (the fabric that integrates system components and carries the communication traffic between them) to facilitate higher security. We observe ...

Keywords: AMBA Bus, access control, architecture, attacks, bus, communication, digital rights management (DRM), intrusion detection, security, security-aware design, small embedded systems, system-on-chip (SoC)

13 [RaceTrack: efficient detection of data race conditions via adaptive tracking](#)



Yuan Yu, Tom Rodeheffer, Wei Chen

October 2005 SOSP '05: Proceedings of the twentieth ACM symposium on Operating systems principles

Publisher: ACM

Full text available: [pdf\(321.34 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bugs due to data races in multithreaded programs often exhibit non-deterministic symptoms and are notoriously difficult to find. This paper describes RaceTrack, a dynamic race detection tool that tracks the actions of a program and reports a warning ...

Keyw ord s: race detection, virtual machine instrumentation

14 [RacerX: effective, static detection of race conditions and deadlocks](#)



Dawson Engler, Ken Ashcraft

December 2003 ACM SIGOPS Operating Systems Review , Volume 37 Issue 5

Publisher: ACM

Full text available: [pdf\(310.63 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

This paper describes RacerX, a static tool that uses flow-sensitive, interprocedural analysis to detect both race conditions and deadlocks. It is explicitly designed to find errors in large, complex multithreaded systems. It aggressively infers checking ...

Keyw ord s: deadlock detection, program checking, race detection

15 [Real-Time Refinement and Simplification of Adaptive Triangular Meshes](#)

Vasily Volkov, Ling Li

October 2003 VIS '03: Proceedings of the 14th IEEE Visualization 2003 (VIS'03) 2003

Publisher: IEEE Computer Society


Full text available: [pdf\(842.85 KB\)](#)


Additional Information: [full citation](#), [abstract](#), [cited by](#)

In this paper we present a generic method for incremental mesh adaptation based on hierarchy of semi-regular meshes. Our method supports any refinement rule mapping vertices onto vertices such as 1-to-4 split or \sqrt{3}-subdivision. Resulting adaptive ...

Keyw ord s: adaptive meshes, refinement and simplification, subdivision, multiresoluton, level of detail, frame-to-frame coherence, out-of-core visualization

16 [Techniques and tools for analyzing intrusion alerts](#)

 Peng Ning, Yun Cui, Douglas S. Reeves, Dingbang Xu
May 2004 ACM Transactions on Information and System Security (TISSEC),
Volume 7 Issue 2
Publisher: ACM


Full text available:  [pdf\(1.55 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Traditional intrusion detection systems (IDSs) focus on low-level attacks or anomalies, and raise alerts independently, though there may be logical connections between them. In situations where there are intensive attacks, not only will actual alerts ...

Keywords: Intrusion detection, alert correlation, security management

17 [Region-based shape analysis with tracked locations](#)

 Brian Hackett, Radu Rugina
January 2005 ACM SIGPLAN Notices, Volume 40 Issue 1
Publisher: ACM


Full text available:  [pdf\(205.67 KB\)](#)


Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
[index terms](#)

This paper proposes a novel approach to shape analysis: using local reasoning about individual heap locations instead of global reasoning about entire heap abstractions. We present an inter-procedural shape analysis algorithm for languages with destructive ...

Keywords: memory leaks, memory management, shape analysis, static error detection

18 [Probabilistic calling context](#)

 Michael D. Bond, Kathryn S. McKinley
October 2007 ACM SIGPLAN Notices, Volume 42 Issue 10
Publisher: ACM


Full text available:  [pdf\(237.78 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


Calling context enhances program understanding and dynamic analyses by providing a rich representation of program location. Compared to imperative programs, object-oriented programs use more interprocedural and less intraprocedural control flow, ...

Keywords: anomaly-based bug detection, calling context, dynamic context sensitivity, intrusion detection, managed languages, probabilistic, residual testing

19 [RaceTrack: efficient detection of data race conditions via adaptive tracking](#)

 Yuan Yu, Tom Rodeheffer, Wei Chen
October 2005 ACM SIGOPS Operating Systems Review, Volume 39 Issue 5

Publisher: ACM


Full text available:  [pdf\(321.34 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
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Bugs due to data races in multithreaded programs often exhibit non-deterministic symptoms and are notoriously difficult to find. This paper describes RaceTrack, a dynamic race detection tool that tracks the actions of a program and reports a warning ...

Keywords: race detection, virtual machine instrumentation

20 [Region-based shape analysis with tracked locations](#)

 Brian Hackett, Radu Rugina
January 2005 POPL '05: Proceedings of the 32nd ACM SIGPLAN-SIGACT symposium on Principles of programming languages

Publisher: ACM

Full text available:  [pdf\(205.67 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#),
[index terms](#)

This paper proposes a novel approach to shape analysis: using local reasoning about individual heap locations instead of global reasoning about entire heap abstractions. We present an inter-procedural shape analysis algorithm for languages with destructive ...

Keywords: memory leaks, memory management, shape analysis, static error detection




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